

**EAST Search History****EAST Search History (Prior Art)**

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	15	("6437462" "6548916" "4958251" "4020395" "3571608" "2002130643").PN.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/09 13:25
S2	652	resistor near parallel near diode	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/08 19:31
S3	41	S2 with gate	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/08 19:31
S4	11	("4504779"   "4540899"   "4890020"   "4904889"   "5027018"   "5204561"   "5313109").PN. OR ("5525925").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/11/09 11:24
S5	1	"6031705".pn.	US-PGPUB; USPAT; USOCR	OR	ON	2007/11/09 11:24
S6	4	("5198957"   "5440441").PN. OR ("6031705").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/11/09 11:25
S7	756	361/91.1.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/09 13:26
S8	49	S7 and ((zener adj diode) with gate)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/09 13:27

S9	33339	protect\$4 with batter\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/06/21 16:37
S10	360	S9 and ((zener adj diode) with gate)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/06/21 16:38
S11	297	S10 and (resistor with diode)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/06/21 16:38
S12	220	S9 and (((zener adj diode) with gate) same (resistor with diode))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/06/21 16:39
S13	5975	protect\$4 with ((multi \$4 plural\$4 two) near2 (voltage power suppl\$4))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/06/21 16:45
S14	355	S13 same (batter\$4 accumulator)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/06/21 16:46
S15	13	S14 same (zener adj diode)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/06/21 16:46
S16	9912	pinch adj off	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/06 16:26

S17	3	S16 with (overvoltage near2 protect\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/06 16:26
S18	2	S16 with (((over adj current) overcurrent) near2 protect\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/06 16:29
S19	62	saturation with (((over adj current) overcurrent) near2 protect\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/06 16:30
S20	8	(saturation near3 (nmos mos transistor)) with (((over adj current) overcurrent) near2 protect\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/06 16:30
S21	616	(saturation near3 (nmos mos transistor)) with limit \$4 with current	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/06 16:45
S22	23	S21 with protect\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/06 16:45
S23	36	((pinch adj off) pinchoff) same (overvoltage (over adj voltage))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/08 15:15
S24	1292	(constant adj current adj source) with fet	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/10/02 15:11

S25	53	S24 with ((pinch\$4 adj off) saturat\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/10/02 15:12
S26	15232	source adj follower	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/10/02 15:48
S27	14	S26 with (voltage adj protect\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/10/02 15:48
S28	915	361/91.1.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/10/02 16:02
S29	19	S28 and (source adj follow\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/10/02 16:02
S30	7	("5416368"   "5629542"   "5880605"   "6031393"   "6064231"   "7098694").PN. OR ("7430100").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2009/10/02 16:05
S31	346	clamp\$4 with (source adj follow\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/10/02 16:08
S32	32	(voltage adj clamp\$4) with (source adj follow \$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/10/02 16:09

S33	1436	bhattacharya.in.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/10/02 16:23
S34	9	S33 and (source adj follow\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/10/02 16:23
S35	8	S28 and (pinch adj off)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/10/02 16:25
S36	11	S28 and (common adj drain)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/10/02 16:33
S37	30636	(source adj follow\$4) (common adj drain)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/10/02 16:36
S38	2079	361/111.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/10/02 16:36
S39	47	S38 and S37	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/10/02 16:36
S40	258	(source adj follow\$4) with (regulat\$4 near2 (voltage output))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/10/02 16:42

S41	0	(source adj follow\$4) with (batter\$4 near2 gate)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/10/02 16:43
S42	150	(source adj follow\$4) with (accumulat\$4 near2 gate)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/10/02 16:44
S43	0	(source adj follow\$4) with (accumulator near2 gate)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/10/02 16:44
S44	3	(source adj follow\$4) with (accumulator with gate)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/10/02 16:44
S45	13	(source adj follow\$4) with (batter\$4 with gate)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/10/02 16:45
S46	1553	361/18.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/10/02 17:12
S47	33	S46 and S37	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/10/02 17:12

**EAST Search History (Interference)**

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp

L1	1	((multi adj voltage) accumulator MOSFET (gate adj resistor) (short adj circuiting) (zener adj diode)). clm.	US-PGPUB; USPAT	AND	ON	2009/10/09 16:04
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